

FACULTY PROFILE

Name of the faculty: P.NISHMIKA

Department: ECE

College: Gudlavalleru Engineering College

Mobile: 9030011407

E-Mail: Pothana.nishmika@gmail.com



RESIDENTIAL ADDRESS: D/No:42-65-10, Block No:65, Ajith Singh Nagar, Vijayawada-15.

ACADEMIC QUALIFICATIONS: M.Tech.

ACADEMIC REWARDS / ACHIEVEMENTS:

- M.Tech (VLSI & Embedded Systems) with an aggregate of 82% from V R Siddhartha Engineering College, JNTUK University in 2021-2023.
- B.Tech (E.C.E) with an aggregate of 69% from V R Siddhartha Engineering College, JNTUK University in 2017-2021.
- Intermediate with an aggregate of 92% from State Board of Intermediate in 2015-2017.
- SSC with an aggregate of 70% Indian Council of Secondary Education in 2014-2015.

PROFESSIONAL EXPERIENCE:

Designation	Institution	Period
Assistant Professor	Gudlavalleru Engineering College	June 21 st 2024 to till date

INTERNSHIP/IN-PLANT TRAINING DETAILS:

Industry: SoCtronics Technologies Pvt. Ltd., Guntur, Andhra Pradesh.

Stream: Design Verification.

Duration: October 10,2022 – May 10,2023.

Industry: NPHSAT SYSTEMS Pvt. Ltd., Vijayawada, Andhra Pradesh.

Stream: Small satellite design.

Duration: March 24,2020 – April 25,2020.

PROFESSIONAL SKILLS:

- Strong knowledge of Verilog HDL, System Verilog, UVM, C/C++, Python
- Synopsys – VCS, Mentor Graphics- Questasim, VC Spyglass Lint
- RTL Coding using Synthesizable constructs of Verilog, FSM based design, Simulation, CMOS Fundamentals, Code Coverage, Functional Coverage, Synthesis.

CERTIFICATE COURSE:

- Advanced VLSI Design and Verification – Maven Silicon
- Digital Circuits - NPTEL

SPECIALIZATION: VLSI DESIGN

RESEARCH PAPERS / BOOKS: -----

PAPERS PUBLISHED IN IEEE CONFERENCE:

Nishmika P and V H Prasad Reddy –“VERIFICATION OF COVERAGE DRIVEN AND ASSERTION - BASED AXI4 PROTOCOL VIP USING UVM”, Published in: 2023 First International Conference on Cyber Physical Systems, Power Electronics and Electric Vehicles (ICPEEV), **ISBN:979-8-3503-2357-3**,
DOI: [10.1109/ICPEEV58650.2023.10391880](https://doi.org/10.1109/ICPEEV58650.2023.10391880)

CERTIFICATIONS/Training Programs/FDPs Attended No:

1. One week Online Faculty Development Program on "**Semiconductor Devices, Modelling and Circuit Technology**" conducted by Lendi Institute of Engineering and Technology, Vizianagaram from 19-08-2024 to 24-08-2024.
2. Three-day Online Faculty Development Program on “**Generative AI**” organized by the Department of Information Technology of Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru from 26 JUN 2024 to 28 JUN 2024.
3. Training in Advanced VLSI Design and Verification from Maven Silicon, Bangalore, from September 2023 to April 2024

LIST OF WORKSHOPS /CONFERENCES /SEMINARS ATTENDED:

- Attended a workshop on “Circuits and Systems(CAS) research forum”, organized by IEEE CAS/EDS, Vijayawada.
- Attended an International Conference on Cyber Physical Systems, Power Electronics and Electric Vehicles (ICPEEV) at Mahindra University, Hyderabad, India.

PAPER PRESENTATION IN SEMINARS:

- Presented a paper on –“VERIFICATION OF COVERAGE DRIVEN AND ASSERTION - BASED AXI4 PROTOCOL VIP USING UVM” at Mahindra University, Hyderabad.

PROFESSIONAL BODIES MEMBERSHIPS:

- Member in “**International Association of Engineers (IAENG)**”.

GUEST LECTURES DELIVERED: ----

CONSULTANCY ACTIVITIES: ----

GRANTS FETCHED: ----

SUBJECTS Handled:

Theory:

1. DI&CO
2. Automotive Electronics(Open elective)

Labs:

1. Electrical and Electronics Workshop(EEW)
2. IOT Lab
3. RFOC Lab



Extra Curricular Activities: Department level activities

Declaration

I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 30-12-2024
Place Gudlavalleru

Nishmika.Pothana
Signature



lendi Institute of Engineering & Technology
An Autonomous Institution
Approved by AICTE & Permanently Affiliated to JNTU Gurajada, Vizianagaram



lendi
IEEE
STUDENT BRANCH

CERTIFICATE OF PARTICIPATION

PROUDLY PRESENTED
TO

Nishmika Pothana

S R GUDLAVALLERU ENGINEERING COLLEGE

has Successfully Participated in the

One Week Online FDP on Semiconductor Devices, Modeling & Circuit Technology

Organized by ECE Department of LENDI INSTITUTE OF ENGINEERING AND TECHNOLOGY, AP
During the period of 19th to 24th August 2024. We commend your dedication to enhancing your knowledge in Semiconductor Devices and Circuit Technologies. Your active participation and engagement during the program have contributed to its Success.

Dr. R. Santosh
Coordinator

Dr. M. Rajan Babu
HOD ECE & Co-Coordinator

Dr. V. V. Rama Reddy
Principal



Seshadri Rao Gudlavalleru Engineering College

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)

Seshadri Rao Knowledge Village, Gudlavalleru -521356.



Certificate of Participation

This is to certify that **Ms. NISHMIKA POTHANA**, Assistant Professor of the Department of **Electronics and Communication Engineering** has successfully participated in the **three-day Online Faculty Development Program on "Generative AI"** organized by the Department of Information Technology of Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru from **26 JUN 2024 to 28 JUN 2024**.

Dr.D.N.V.S.L.S.INDIRA
HOD, IT

Dr.B.KARUNA KUMAR
PRINCIPAL

Mr.D. SAI SATHISH
FOUNDER & CEO OF INDIAN
SERVERS

Date: 21/June/2024

Place: Vijayawada.

To
The Principal,
SRGEC,
Gudlavalleru,

Respected Sir,

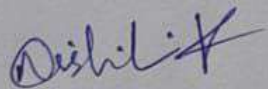
Sub: Joining letter for the post of Assistant professor in ECE Dept
Reg NO: SRGE/EST/2024 dt. 21/06/24.

I hereby with confirm my joining as Assistant professor in ECE department. I attaching ~~by~~ the original certificates.

I give the commitment that I shall efficiently discharge my responsibilities & duties. I also assure you that I will abide by all the rules & regulations of your organization.

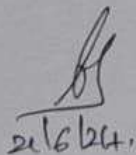
Thanking you,

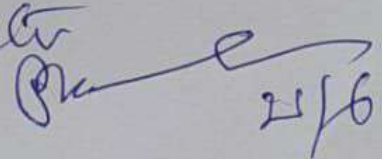
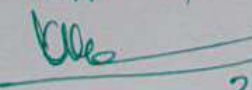
Yours faithfully,



[NISHMITA.P]

Forwarded to Vice Principal Administration


21/6/24.

Submitted to Principal for
consideration
Approved 
21/6

21/6/24.

SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE



(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)

Seshadri Rao Knowledge Village, GUDLAVALLERU-521 356, Krishna District, A.P., India

(Approved by AICTE, New Delhi and Permitted by A.P. State Government)

Accredited by NAAC

Phone Nos. 08674-273737, 273888, Mobile: +91 9848778121 Fax No. 08674-273957

e-mail: principal@gecgudlavalluru.ac.in, office@gecgudlavalluru.ac.in, Web: www.gecgudlavalluru.ac.in

VALLURUPALLI RAMAKRISHNA
Co-Secretary & Correspondent

Ref. SRGEC/EST/2024/

18th June 2024

Sub: Establishment-Seshadri Rao Gudlavalluru Engineering College – Recruitment of Teaching Faculty - Appointment orders – Issued.

Ref: College Staff Selection Committee Held on 18-06-2024.

:: ORDER ::

With reference to the above, **Ms. NISHMIKA POTHANA** is appointed as **Assistant Professor** in the department of **Electronics and Communication Engineering** on a basic pay of **Rs.15,600/-** in the pay scale of **15600-39100+6000 AGP**.

Her appointment is subject to the rules and regulations of the college that are in force and may come into force from time to time.

She is further informed that she should perform any additional duties, either remunerative or non-remunerative, in addition to her normal teaching duties that may be assigned to her by the college from time to time.

She has to register for Ph.D program with State/Central Universities/Institutions within two years from the date of joining.

She shall report to duty on or before 25th June 2024 failing which her appointment is liable to be cancelled.

She shall produce the certificates of educational qualifications, memoranda of marks, certificates of experience, category, date of birth etc. and certificate of relief from the previous employer, if any, all in original, to the Principal at the time of reporting to duty.

Her performance will be reviewed at the end of the Academic year for three successive years on

1. Theory subjects taught and laboratories handled, on teaching-learning methodologies they have adopted including learning materials they have prepared and supplied to the students.
2. English Communication Skills
3. Securing NPTEL certification in two/three subjects she teaches with Elite + Gold or very close to it in two years.
4. Ability to teach and train students for all competitive examinations in three years.
5. Competencies as per Bloom's Taxonomy and application of the same in the teaching in three years.

V. Ramakrishna

Co- SECRETARY & CORRESPONDENT

N

Copy to

i) Ms. P. Nishmika,
Door No.42-65-10, Block No.65,
Ajith Singh Nagar,
VIJAYAWADA – 520 015,
NTR District.

ii) Principal (EST file)

ii) Concerned, HOD

v) Personal file

CC476203

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA - 533 003, ANDHRA PRADESH, INDIA



Ms. Nishmika Pothana

D/o. PVD RK Srinivas

having fulfilled the academic requirements and passed the examination

held during July 2023 in First Class with Distinction

has this day been admitted by the executive council to the degree of

Master of Technology

(VLSI Design & Embedded Systems)

Given under the Seal of the University



HTNo: 218W1D7708

Date: 23-03-2024



CONTROLLER OF EXAMINATIONS

DIRECTOR OF EVALUATION

REGISTRAR

CC479140

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA - 533 003, ANDHRA PRADESH, INDIA



Ms. Nishmika Pothana

D/o. PVD R K Srinivas

having fulfilled the academic requirements and passed the examination

held during August 2021 in First Class

has this day been admitted by the executive council to the degree of

Bachelor of Technology

(Electronics & Communication Engineering)

Given under the Seal of the University



HTNo: 178W1A0442

Date: 14-05-2024



N. R. K. V. C.

CONTROLLER OF EXAMINATIONS

[Signature]

DIRECTOR OF EVALUATION

[Signature]

REGISTRAR

Board of Intermediate Education

ANDHRA PRADESH, INDIA

Sl.No. Q141481



Aadhaar No:
599440254771

INTERMEDIATE PASS CERTIFICATE CUM MEMORANDUM OF MARKS

This is to certify that NISHMIKA POTHANA

Father Name : P V D R K SRINIVAS

Mother Name : NIRMALA POTHANA

Registered No. 1705213206

Examination held in MARCH-2017

with ENGLISH

The subjects in which

SHE

bearing

has appeared at the Intermediate Public
and passed in A GRADE
as the medium of instruction.

was examined and the marks awarded are as follows :

Subject	I Year		II Year	
	Maximum Marks	Marks Secured	Maximum Marks	Marks Secured
Part - 1 : ENGLISH	100	088	100	090
Part - 2 : SANSKRIT	100	096	100	097
Part - 3 : Optional Subjects				
MATHEMATICS - A	075	071	075	071
MATHEMATICS - B	075	044	075	069
PHYSICS	060	057	060	058
CHEMISTRY	060	056	060	056
PHYSICS PRACTICAL			030	029
CHEMISTRY PRACTICAL			030	030
ENVIRONMENTAL EDUCATION			Q U A L I F I E D	
ETHICS AND HUMAN VALUES			Q U A L I F I E D	
Total Marks In Figures				
In words	*NINE**ONE***TWO*			
Date	13-04-2017			

Nishmika
PRINCIPAL
Sri Chaitanya Jr. College
Dussehra Venkateswara Street
Signature of the Principal and College Seal

Shankar
Controller of Examinations

NOTE : ELIGIBILITY RULES AND OVERLEAF *INDICATES MARKS OBTAINED AT AN EARLIER EXAMINATION.

No. TG 30025398



1153351/065

025399

PASS CERTIFICATE

No divisions are awarded

This is to certify that **NISHMIKA POTHANA**
of **ST. ANN'S SCHOOL, AJITSINGH NAGAR, VIJAYAWADA**

UNIQUE ID 6032478

Daughter of
Smt **NIRMALA POTHANA**
Shri **P V D R K SRINIVAS**

was awarded an

INDIAN CERTIFICATE OF SECONDARY EDUCATION (CLASS - X)

The candidate attained at least grade **7** in **SIX** subjects of the External Examination and at least grade **D** in the subject of Internal Assessment as given below:

SUBJECTS

External Examination

ENGLISH
TELUGU
HISTORY, CIVICS & GEOGRAPHY
MATHEMATICS
SCIENCE
COMPUTER APPLICATIONS

GRADE

5 FIVE
3 THREE
3 THREE
5 FIVE
5 FIVE
2 TWO

Internal Assessment

SUPW & COMMUNITY SERVICE

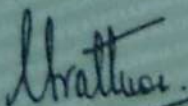
A

Date of birth as

certified by the (in words) **Ninth August Two Thousand**
Head of the School
at the time of (in figures) **09.08.2000**
registration

EXAMINATION OF MARCH 2015

Date of Issue - **18.05.2015**



Chief Executive & Secretary



Chairman
Council for the Indian School
Certificate Examinations



भारत सरकार

Government of India



Issue Date: 28/09/2011



పోతన నిష్మిక

Pothana Nishmika

పుట్టిన తేదీ / DOB: 09/08/2000

స్త్రీ / Female



5994 4025 4771



5994 4025 4771

मेरा आधार, मेरी पहचान



भारतीय विशिष्ट पहचान प्राधिकरण

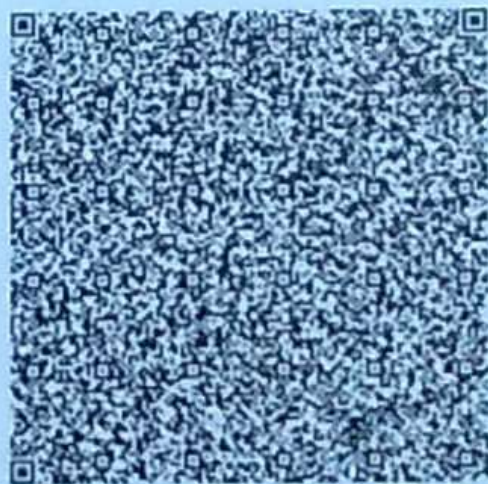
Unique Identification Authority of India



Print Date: 07/12/2021

చిరునామా: D/O పి వి డి ఆర్ కే శ్రీనివాస్, 42-65-10
బ్లక్-65, ఎ ఎస్ నగర్, అజిత్ సింగ్ నగర్,
విజయవాడ అర్బన్, కృష్ణ, ఆంధ్ర ప్రదేశ్, 520015

Address: D/O P V D R K Srinivas, 42-65-10
blockno-65, a s nagar, ajith singh nagar,
Vijayawada (Urban), Krishna, Andhra
Pradesh, 520015



5994 4025 4771



1947



help@uidai.gov.in



www.uidai.gov.in

आयकर विभाग
INCOME TAX DEPARTMENT

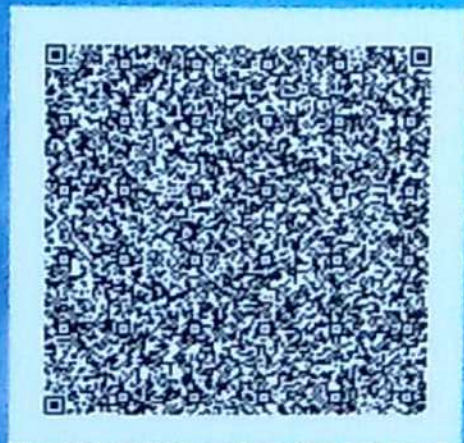


भारत सरकार
GOVT. OF INDIA



स्थायी लेखा संख्या कार्ड
Permanent Account Number Card

ECWPP1142Q



नाम / Name

POTHANA NISHMIKA

पिता का नाम / Father's Name

VENKATA DURGA RAMAKRISHNA SRINIVAS
POTHANA

जन्म की तारीख / Date of Birth

09/08/2000


हस्ताक्षर / Signature

46241



In case this card is lost / found, kindly inform / return to :

**Income Tax PAN Services Unit, UTIITSL
Plot No. 3, Sector 11, CBD Belapur,
Navi Mumbai - 400 614.**

**इस कार्ड के खोने/पाने पर कृपया सूचित करें/लौटाएं :
आयकर पैन सेवा यूनिट, UTIITSL
प्लॉट नं: ३, सेक्टर ११, सी.बी.डी.बेलापुर,
नवी मुंबई-४०० ६१४.**

Aaykar Sampark Kendras

**For Income Tax Related
Queries call Toll Free Nos.**

1961

or

18001801961



SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

Seshadri Rao Knowledge Village
GUDLAVALLERU, Krishna (Dist).A.P.-521356
Pay slip for the month of December 2024

NAME: Ms. Nishmika Pothana
DESIGN: Assistant Professor
DEPT: Electronics & Communication Engineering
PAN NO: ECWPP1142Q
BANK ACC NO: 40675442750
UAN NO: N.A
EPF No. N.A
ESIC NO. N.A

Allowances	Amount	Deductions	Amount
Basic/Consolidated	15,600.00	TDS	0.00
AGP	6,000.00	PT	200.00
DA	0.00	EPF	0.00
HRA	0.00	ESIC	0.00
Variable Pay	0.00	LIC	0.00
		LOP	697.00
		Advance & Others	3,775.00
		Gec Staff Association	0.00
		Total Deductions	4,672.00
Gross	21,600.00	Net Salary	16,928.00

Checked By 


Principal